



Call for Contributions for the Ph.D Forum @ 20th International Symposium on VLSI Design and Test (VDAT-2016), IIT Guwahati (May 24-27, 2016)

Ph.D Forum proposals on contemporary topics and recent developments in the field of VLSI Design, Automation, Test & Verification are invited from research scholars in India.

Submission Details: Ph. D Forum proposals should be submitted to **vdat2016@gmail.com** on or before the deadline date (08.05.2016). The submission should be a SINGLE pdf file that contains

- First page: Full contact address of the scholar, affiliation, phone number, email, thesis supervisor(s) name, title of the Ph.D forum paper and details of all the publications of the scholar relevant to the thesis
- Next two pages: Title and summary describing the novelties and advantages of the thesis work. Figures and tables may be included but should not exceed two pages.
- Copy of ONE published paper of the scholar relevant to the thesis.

Eligibility:

The following 2 category of scholars working in the area of VLSI Design, Test, Embedded Systems, Nano-electronics, or Computer Aided Design are eligible

- Scholars who have finished their PhD thesis within the last 24 months.
- Scholars who are completing their thesis work in another 24 months.

Benefits:

The participants of the PhD forum may enjoy the following benefits:

- A poster and oral presentation (with demo if applicable) at the PhD forum
- Contacts to professionals from industry and academia
- Feedback and comments on your work by experts working in the domain
- Distribution of leaflets summarizing the PhD work
- Award(s) sponsored by (TBA) for the best poster(s)

Important Dates:

- Proposal submission deadline : **08.05.2016**
- Notification of acceptance : **10.05.2016**
- Camera ready version and registration : **14.05.2016**
- Ph.D Forum presentation at VDAT 2016 : **27.05.2016**

For further details regarding Ph.D forum contact Dr. John Jose, IIT Guwahati (johnjose@iitg.ernet.in)