Indian Institute of Technology Guwahati
Research and Development Section
Guwahati-781039, Assam

Applications are invited for **Walk in interview** for the following post in the project entitled, "Securing Low Power Embedded Processors for IoT Applications Against Power-Analysis Attacks" at the Department of Electronics and Electrical Engineering, IIT Guwahati

Date: 22nd May 2025 (Thursday)
Time: 10:00 am onwards
Venue: EEE Conference Room

S. N o	Designati on	No. of Vaca ncies	Pay Recom mende d (Rs.)	HRA Requir ed (Rs.)	Med. Requir ed (Rs.)	Total Amount (Rs.)	Duration of Appoint ment in months	Qualification
1	**Assistant Project Engineer**	01	28500	5130	1250	34880	11	B.E/B.Tech in CSE/ECE. Experience/Knowledge in Coding experience in C++ and Python, Firmware Experience and Interfacing with CNC machine, etc. are preferred

How to apply and selection process: Candidates must appear for the walk-in interview. They have to send an application/CV giving details of all educational qualifications (starting from 10th std), experience, contact address, phone no., e-mail, etc. By 20th May 2025 at 5 PM, send the PI to the following email address. Shortlisted candidates will be called for the interview through e-mail.

Selection will be based on the performance of the candidate in the interview.

For any clarification, contact: Prof. Roy Paily Palathinkal (Principal Investigator) Email: roypaily@iitg.ac.in or Phone: (0361) 2582512.

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