

## Indian Institute of Technology Guwahati Research and Development Section Guwahati-781039, Assam

Applications are invited for a Walk-in/Online interview for the following post(s) in the project entitled, "Design and Development of AI/ML Co-Processor and Post Quantum Cryptography Co-Processor (An Initiative towards Electronics System Design and Manufacturing in North-East Region)" at the Department of Electronics and Electrical Engineering, IIT Guwahati.

Date: - 12 September 2025 (Friday)

Time: 10.00 am onwards

**Venue: Walk-in Interview (E&ICT Conference Room, Technology Complex)**. WebEx/Google Meet link will be sent by email to the shortlisted candidates for the online interviews.

Sl. No.	Project Staff Designation	Number of Vacancies	Pay (Rs.)	HRA (Rs.)	Medical (Rs.)	Total (Rs.)	Duration in months	Qualifications
1	Project Scientist	2	56000	11200	1250	68450	6 Months	(1) Ph.D. in VLSI/Microelectronics/CS, OR MTech/ME in VLSI/Microelectronics/ CS with 2 Years of relevant experience.
2	Associate Project Engineer	1	35000	7000	1250	43250	6 Months	(1) Master's degree in VLSI/Microelectronics/CS OR (2) Bachelor's degree in EC/EE/CS with 3 years exp in RTL Design/Data Verification or DFT/Synthesis.
3	Scientific Administrat ive Assistant	2	18000	3600	1250	22850	6 Months	(1) MSc/BSc in Electronics/CSE/IT OR (2) Diploma in Engineering and Technology, preferably with min. 1 years of work experience

## How to apply and selection process:

Interested candidates may apply for the online/in-person interview at the link given below for the corresponding positions giving details of all educational qualifications, experience, contact address, phone no., email etc.

Form Link

https://forms.gle/wZknKgTFPXwFfNZR8

Candidates need to upload the relevant documents in PDF format only at the registration link given above on or before **10 September 2025 (Wednesday) till 05.00 PM.** Candidates will be shortlisted based on the qualifications posted in the advertisement and, later only shortlisted candidates will be intimated by email separately to attend interviews.

Candidates applying for multiple posts will not be considered for selection.

The last date for applying for the above-mentioned positions is 10 September 2025 (Wednesday) till 05.00 PM.

**Selection Process:** The shortlisted candidates have to appear in the online or in-person interview. A choice must be made during the online form submission. Selection will be based on the performance of the candidate in the interview. The selection committee will decide on a suitable candidate after the interview. Candidates will not be sent any call letter separately.

For any clarification, contact the office of Prof. Gaurav Trivedi (Principal Investigator)

Email: piaipqcmeity@iitg.ac.in ninelabsiitg@gmail.com

Phone: 9395618834 (Timing: - 10 A.M to 5 P.M)

No campus accommodation will be available for the selected candidates. No TA/DA will be paid to the candidates for appearing in the test and interview.

EEESPNMEIT00883xxGT008-ZBSA-2354

**HOS&Assistant Registrar (R&D)**