भारतीय प्रौद्योगिकी संस्थान गुवाहाटी गुवाहाटी-781039, असम

Indian Institute of Technology Guwahati Guwahati-781039, Assam

औद्योगिक सहभागिता तथा विशेष पहल कार्यालय

Office of the Industrial Interactions and Special Initiatives

Applications are invited for an **Offline Interview** for the following post(s) in the project entitled "**Design of the IPs of post-quantum-cryptography algorithms on FPGAs and development of its software library using C/C++/Python**" at the Dept. of Electronics and Electrical Engineering; IIT Guwahati

Date: 10th May, 2024 Time: 10:00 AM onwards

Mode: Offline, Venue: EEE Department, IIT Guwahati-781039

SIN	Project Staff Designation	Number of Vacancies	Pay Scale (₹)	Actual Pay recommended *	HRA	Medical Facility	Duration of Appointm ent	Qualifications
1.	Assistant Project Engineer	1	28500-1350- 42000	31000	16% of Basic	yes	11months	Essential Qualification: Bachelor`s degree in Engineering/Design

How to apply and selection process: Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the time of interview on 10 May 2024 (Friday) at 10.00AM. Venue: Offline. Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV may be sent to the Principal Investigator.

For any clarification, contact: Gaurav Trivedi (Principal Investigator)

Email: trivedi@iitg.ac.in Phone: 8011000783

No TA/DA will be paid to the candidates for appearing in the test and interview

Project No: xEEEISPINTE00883xxGT102 AR (II&SI)

Advt. No: IITG/II&SI/Project Staff Rectt-2024/28