



Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for an **Online interview** for the following post(s) in the project entitled, "**Design and Development of AI/ML Co-Processor and Post Quantum Cryptography Co-Processor (An Initiative towards Electronics System Design and Manufacturing in North-East Region)**" at the Department of Electronics and Electrical Engineering, IIT Guwahati.

Date: 30 November 2024 (Saturday)

Time: 10.00 am onwards

Venue: WebEx/Google Meet link will be sent by email to the shortlisted candidates for the online interviews.

Sl. No.	Project Staff Designation	Number of Vacancies	Pay (Rs.)	HRA (Rs.)	Medical (Rs.)	Total (Rs.)	Duration in months	Qualifications
1	Sr. Project Engineer	1	75,000	13500	1250	89750	6 Months	(1) Ph. D in EC/EE/CS OR (2) MTech/ME in VLSI /Microelectronics/CS with 4 Years of relevant experience OR (3) BTech/BE in EC/EE/CS with 6 years relevant experience. Candidates having knowledge of chip signoff/tape-out and the commercial EDA tools and the VLSI Design flow are preferred.
2	Project Scientist	1	56000	10080	1250	67330	6 Months	(1) Ph.D. in VLSI/Microelectronics/CS, OR MTech/ME in VLSI/Microelectronics/CS with 2 Years of relevant experience OR (2) BTech/BE in EC/EE/CS with 4 years relevant experience. Candidates having knowledge of chip signoff/tape-out and the commercial EDA tools and the VLSI Design flow, RTL Design/Data Verification OR DFT/Synthesis OR Physical

								Design, and Signoff are preferred.
3	Assistant Project Engineer	1	28500	5130	1250	34880	6 Months	Bachelor's degree in EC/EE/CS with background of RTL Design/Data Verification OR DFT/Synthesis OR Physical Design, Signoff

How to apply and selection process:

Interested candidates may apply for the online interview at the link given below for the corresponding positions giving details of all educational qualifications, experience, contact address, phone no., email etc.

Form Link

<https://forms.gle/nsgv7eVEXJV27YsA8>

Candidates need to upload the relevant documents in PDF format only at the registration link given above on or before **28 November 2024 (Thursday) till 02.00 PM**. Candidates will be shortlisted based on the qualifications posted in the advertisement and, later only shortlisted candidates will be intimated by email separately to attend interviews.

Candidates applying for multiple posts will not be considered for selection.

The last date for applying for the above-mentioned positions is 28 November 2024 (Thursday) till 02.00 PM.

Selection Process: The shortlisted candidates have to appear in the online interview. Selection will be based on the performance of the candidate in the interview. The selection committee will decide on a suitable candidate after the interview. Candidates will not be sent any call letter separately.

For any clarification, contact the office of Dr. Gaurav Trivedi (Principal Investigator)

Email: piaipqcmeity@iitg.ac.in

ninelabsiitg@gmail.com

Phone: +91-7086509304/7350390590 (**Contact time: 10.00 AM – 6.00 PM**)

No campus accommodation will be available for the selected candidates.

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Assistant Registrar (R&D)