



Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for an **Online interview** for the following post in a DST funded project entitled, “Machine-Learning Enabled Next-Generation IoT Communications” at the **Department of Electronics and Electrical Engineering, IIT Guwahati.**

Date: 19 June 2024 (Wednesday)

Time: 10 .00 am onwards

Online Interview: Link will be provided to the shortlisted candidates

Sl. No.	Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA (Rs.)	Medical (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	JRF(GATE)	01	37,000	6,660	0	43660	11	BE/B.Tech or ME/ M.Tech or MSc.in relevant area with one of the following: <ul style="list-style-type: none">Valid GATE score in past orSuccessful selection process through National Level examinations conducted by Central Government Departments and their Agencies and institutions such as DST, DBT, DAE, DOs,DRDO, MHRD, ICAR, ICMR, IISc,IISCR etc.

How to apply and selection process: Eligible candidates have to apply with a CV/Resume including details of all educational qualifications, experience (if any), contact address, phone no., E-mail etc. along with scanned copies of relevant documents at email address (kuntaldeka@iitg.ac.in) (with subject: Application for the JRF Recruitment) before **18 June 2024** (1700 Hrs). Shortlisted candidates will be called for an Online Interview on **19 June 2024** (Wednesday) at **10 .00 AM** onwards.

Selection will be based on the performance of the candidate in the interview.

Note:

1. Only shortlisted candidates will be sent call letters.
2. No TA/DA will be paid to the applicants for appearing in the online interview.
3. Recruitment of selected candidate will be subjected to verification of original certificates.

For any clarification, contact:

Dr. Kuntal Deka (Principal Investigator)

Department of Electronics and Electrical Engineering, IIT Guwahati

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Assistant Registrar (R&D)