Manash Ivoti Pathak Assistant Registrar

भारतीय प्रौद्योगिकी संस्थान गुवाहाटी, गवाहाटी-781 039

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: ar_rnd@iitg.ac.in Email

गुवाहाटी-781039, भारत Guwahati-781039, India



Date: 21.11.2023 To,

All interested Vendors Refn: IITG/RND/T118/23-24

SUBJECT: Notice inviting quotation for supply and installation of Virtex FPGA Evaluation Kit and NVIDIA Jetson AGX Orin Developer Kit for the Department of Electronics and Electrical Engineering, IIT Guwahati

Dear Sir/ Madam,

The Indian Institute of Technology Guwahati, invites quotations for supply and installation of Virtex FPGA Evaluation Kit and NVIDIA Jetson AGX Orin Developer Kit as per details mentioned in the Annexure-I, attached herewith. The quotation is to be submitted online as per 'Instruction to Bidders', 'Terms & Conditions' and Annexure-I, II, III, IV, and V on or before 13.12.2023 at 12:00 HRS.

Kindly read the NIQ document carefully before participating. It shall be deemed that submission of bid by the bidder has been done after their careful study and examination of the NIQ terms with full understanding to its implications. Any lack of information shall not in any way relieve the bidder of its responsibility to fulfill its obligations under the Bid.

The Director IITG will reject a proposal for award if it is determined that the bidder recommended for award has engaged in corrupt or fraudulent practices in competing for, or in executing, the Purchase Order. Fraudulent Practice means a misrepresentation of facts in order to influence a procurement process and includes collusive practice among bidders (prior to or after Bid submission) designed to establish Bid prices at artificial non-competitive levels and to deprive IITG of the benefits of free and fair competition. Corrupt Practice means offering, giving, receiving or soliciting of anything of value, pressurizing to influence the action of a public official in the process of this purchase execution.

INSTRUCTION TO BIDDERS

01. Mode of bid: Tender will have to be submitted in **Double bid.**

02. Important Date, Time and Venue:

SI. No.	Particulars	Date & Time
01	Date of uploading of NIQ & other documents	21.11.2023 at 18.00 hrs
02	Documents download/sell start date	21.11.2023 at 19:00 hrs
03	Documents download/sell end date	12.12.2023 at 12:00 hrs
04	Pre-Bid Meeting	N/A
05	Bid submission start date	24.11.2023 at 18:00 hrs
06	Bid submission closing	12.12.2023 at 12:00 hrs
07	Technical Bid Opening date	13.12.2023 at 14:00 hrs
08	Venue of Technical bid opening	Department of Electronics and Electrical Engineering, IIT
	, ,	Guwahati
09	Financial Bid Opening date	To be informed later on

- 03. Submission of bids: Bids are to be submitted through online to the website https://eprocure.gov.in/eprocure/app in Two Covers, one in Fee/Technical Cover & the other is Financial Cover before the prescribed date and time using the Digital Signature Certificate (DSC). The documents duly digitally signed are to be uploaded.
- 03.1. Fee / Technical Bid: The technical Cover should contain scanned PDF copies (Single pdf file for multiple pages and documents) of the following in two folders. The seal and signature of the authorized official of firms must appear on all the papers uploaded.

03.1.A. Fee/Technical Documents:

i. Scanned copy of EMD. If any bidder is exempted from payment of EMD, Relevant Document(s) needs to be uploaded as mentioned in sl. no. 08 of the Terms & Condition.

- ii. Compliance certificate [Annexure -II]
- iii. Manufacturer's Certificate in case of manufacturer or Manufacturer Certificate along with Authorized Dealership Certificate on the offered products in case of Agent or Dealer.
- iv. Details of the technical features of the offered item in the format at Annexure-I
- v. Standard Technical leaflet/literature of the item offered
- vi. HSN/SAC Number, ISO/ISI Certificate and Proof of Certification as per specification
- vii. Warranty Details.

03.1.B. Other Important Documents (OID):

- i. Bidder's detail and its Service Centre detail as per format at Annexure-III
- ii. Character certificate (Undertaking that currently the vendor is not blacklisted by any govt. organization/institution)
- iii. GST Registration Certificate and GST Clearance certificate.
- iv. PAN Detail and TIN number
- v. Registration Certificate
- vi. Banker's Detail
- vii. List of reputed organizations/institutions, particularly to IIT/Institutes and other Government Organization where similar orders have been executed, if any (copy (s) of the Purchase Orders and Installation certificates will have to be uploaded).
- **03.2. Financial Cover:** The financial Cover should contain Bill of Quantities (BOQ) **in excel format.** The bidder is to quote the rate in the space marked for quoting rate in the BOQ. Only downloaded copy with same file name of BOQ document must be uploaded by the bidder after filling up.

04. Evaluation of Process:

- 04.1. Opening of Technical Cover: Technical Documents will be opened by the Purchase Committee members using Digital Signature Certificate (DSC).
- 04.2. Cover for Fee/Technical Documents (Ref. Sl. No. 03.1.A) and cover for Other Important Documents (Ref. Sl. No. 03.1.B.) will be opened. If there is any deficiency/discrepancy in the Documents, the bidder will summarily be rejected.
- 04.3. Decrypted (transformed into readable formats) documents of the covers will be downloaded and handed over to the Purchase Committee.
- 04.4. Summary list of technically qualified bidders will be uploaded online.
- 04.5. During evaluation the Committee may summon the bidders and seek clarification/information or additional documents or original hard copy of any of the documents already submitted & if these are not produced within the stipulated time frame, their proposals will be liable for rejection.
- 04.6. The Financial offer of the prospective bidder will be considered only if the Technical Bid of the bidder is found qualified by the Purchase Committee.
- 05. Award: The Final Award will be given to the vendor, selected by the Purchase Committee on the lowest quote basis.

06. Communication:

- (i) For any technical clarification, please contact at-rafiahamed@iitg.ac.in, Tel.No. +91-361-2582520
- (ii) In all communication to and from IIT Guwahati, the following email IDs are to be put in loop mandatorily.
 - (a) ar rnd@iitg.ac.in
 - (b) priyadevi@iitg.ac.in

TERMS & CONDITIONS

01. Rates: Rates quoted should be on F.O.R. Department of Electronics and Electrical Engineering, IIT Guwahati, IIT Guwahati basis for indigenous items and on DOOR DELIVERY PAID basis till the site of location at Department of Electronics and Electrical Engineering, for imported items, with break-ups as per BOQ. Failing to provide as per BOQ, quotation may not be accepted.

Currency Conversion against Item in Column K of BOQ Excel file there is two conversion type: 1. If you choose "Full Conversion" then Column BA (AMOUNT) and Column BB (TAXES) BOTH values will be converted based on selected Currency in (Column L). 2. If you choose "Partial Conversion" then Column BA (AMOUNT) only will be converted based on selected currency (Column L) and Column BB (TAXES) value (INR) will be added to the converted values. If any bidder quotes all the Amount in INR, the bidder must select "Full Conversion" type in currency conversion in column K.

02.Validity of Quotation: Quoted rates must be valid for **120 days** from the last date of submission of quotation.

03.Warranty: Please refer to Annexure I (Technical Specifications).

- **04.** Literature a must: All the quotations must be supported by technical leaflet/literature and the specifications mentioned in the quotation must be reflected/ supported by such technical leaflet/ literature. The model and specifications quoted should **invariably be highlighted** in the leaflet/literature for easy reference.
- **05. After Sales Service**: The bidders should clearly state the detail address, contact number and email ids of after sales service centre in India in Bidder's Details under Statutory Cover as at **Annexure-III** without which their offers shall be liable for rejection.
- **06. Dealership Certificate**: Dealers or Agents quoting on behalf of Manufacturer must upload valid dealership certificate.
- **07. Quality Certificates**: Valid certificate to prove that the products are genuine and of International standard, as mentioned below, must be uploaded: (a) Manufacturer's certificate; (b) ISO/ISI certificate.
- **08. Earnest Money:** An Earnest Money Deposit (EMD) for ₹10,000.00 (Rupees Ten Thousand only) in the form of Bank Guarantee as Annexure-IV OR an Account Payee Demand Draft OR Fixed Deposit Receipt from a Commercial bank in favor of "Registrar IIT Guwahati", payable at IIT Guwahati, will have to be submitted to The Assistant Registrar, Research & Development Section, IIT Guwahati, Guwahati 781039. For items treated as part of the same unit the total values of all items taken together will be treated as the value for EMD amount. Offers received without Earnest Money or valid Certificate shall be summarily rejected.

EMD of unsuccessful bidder will be returned within one month of placement of Purchase Order. In case of the successful bidder the EMD will be returned on receipt of the PBG.

If exemption is sought from production of EMD, the same should clearly be mentioned indicating the clause under which exemption is sought. EMD is exempted under the following conditions: -

- (i) Firms which are specifically registered for the required items with DGS&D or with NSIC Copy of valid registration must be uploaded in place of EMD document.
- (ii) Vendors have successfully executed two Purchase Orders in the last three Financial Years in IIT Guwahati, copies of the PO must be uploaded in place of EMD document.
- **09. Performance Bank Guarantee:** The successful bidder, to whom the PO is placed, shall furnish an unconditional Performance Bank Guarantee from a scheduled Bank of India, as per the below mentioned slab, within 21 days of placement of order. The format for PBG, unless otherwise approved by the competent authority, shall be as per the format enclosed at **ANNEXURE-V**. In case of foreign purchase, the local Agent shall submit the PBG, where the PBG is to be necessarily given by a foreign bank, it shall be endorsed by its counterpart in India. Validity of the PBG, which shall be effective from the date mentioned therein, shall be **warranty period + 2 months**.

In exceptional case or on the request of the Indenting Officer competent authority may allow starting of PBG Period from the date of installation, in which case submission of PBG valid for *warranty period* + 2 *months* may be made within 21 days from the date of installation.

- (i) By submitting the PBG, the vendor is understood to have guaranteed that,
- (a) The Purchase Order (PO) shall be executed as per terms and conditions mentioned therein.
- (b) The equipment shall function satisfactorily for a period up to 60 days after the warranty period.
- (c) The equipment and components are free from poor workmanship, bad quality, and faulty designs.
- (d) The vendor shall at his/their own cost rectify/replace the defects, if any, during the guarantee period.
- (e) The guarantee is to the extent of the below mentioned slab.
- (ii) Condition for invoking PBG: In case of failure to comply with the guarantees above, IITG may terminate the contract / purchase order in whole or in part and forfeit the PBG. In addition, IITG may, at its discretion, procure upon such terms and in such manner as it deems appropriate, goods similar to the undelivered items/products and the defaulting supplier/vendor shall be liable to compensate IITG for any extra expenditure involved.

```
Slab 1: PO value (1.01 lakh- 5.00 lakhs) ------ PBG Rate: 3% Slab 2: PO value (5.01 lakh- 7.50 lakhs) ------ PBG Rate: 5% Slab 3: PO value (7.51 lakh- 10.00 lakhs) ------PBG Rate: 7.5% Slab 4: PO value (10.01 lakh-14.99 lakhs) ------PBG Rate: 8.5% Slab 5: PO value (15 lakhs and more) -------PBG Rate: 10%
```

10. Delivery:

a) Time Limit: Maximum within 45 days from the date of issue of purchase order in case of indigenous items and 90 days for imported items.

- b) Safe Delivery: All aspects of safe delivery shall be the exclusive responsibility of the vendor. At the destination site, the package will be opened only in the presence of IIT user/representative and vendor's representative. The intact condition of the package and the seal/indicators for not being tempered with shall form the basis for certifying the receipt in good condition.
- c) Insurance: The supplier is to establish 'All Risk Transit Insurance' coverage till door delivery at the site of location of Department of Electronics and Electrical Engineering, IIT Guwahati
- d) Part Delivery: Part delivery is not allowed.
- e) Penalty Delay Delivery: The date of delivery should be strictly adhered to otherwise the Director, IITG reserves the right not to accept delivery in part or full.
- 11. Genuine Pricing: Vendor is to ensure that quoted price is not more than the price offered to any other customer in India to whom this particular item has been sold, particularly to IIT/Institutes and other Government Organization. Copy of the latest price list for the quoted item, applicable in India, must be uploaded with the offer.
- **12. Conditional tenders not acceptable:** All the terms and conditions mentioned herein must be strictly adhered to by all the vendors. Conditional tenders shall not be accepted on any ground and shall be rejected straightway. Printed conditions mentioned in the tender bids submitted by vendors will not be binding on IITG.

13. GST Inward Permit & Entry Tax:

- (a) GST Inward Permit: GST Inward Permits (Way bills) shall be issued as applicable for delivery of materials against receipt of Final Invoice/ Bill.
- (b) Entry Tax: Assam Govt. Entry Tax [to be paid by IIT Guwahati, not by the vendor], wherever applicable, will be added while evaluating cost status of the concerned equipment to be supplied by vendors from outside the State of Assam.

14. GST:

- (a) Up-to-date Sales Tax clearance certificate, GST Registration Certificate indicating also the GSTIN number of the firm must be clearly mentioned in the quotation, Certificates must be uploaded.
- (b) GST Deduction at source as per Order/notification of the Govt. of India will be applicable.
- (c) GST No of IIT Guwahati is 18AAAJI0130P1Z8
- (d) HSN/SAC No of the items must be clearly mentioned in the quotation along with GST No.
- **15.** Late and delayed tender: Late and delayed tender will not be considered.

16. Payment to Local supplier:

- (i) 100% Payment within 45 days from the date of successful delivery, installation and commissioning/ acceptance of goods at IIT Guwahati, generally through RTGS / NEFT, **OR**
- (ii) 90% Payment against delivery and 10% payment after successful installation and/ or commissioning of the ordered goods at site.

Please note as per Institute's norm advance payment is not allowed for indigenous purchase.

- 17. Penalty for delayed delivery: The date of delivery should be strictly adhered to. In the event of delayed delivery, installation & commissioning, the vendor shall be liable for a penalty deduction at a percentage of the value of the undelivered equipment subject to a maximum of 10% (ten percent) as detailed below:
 - @ 1% up to one week;
 - @ 2.5% up to two weeks;
 - @ 5% up to three weeks:
 - @ 10% for four weeks and above

For the purpose of this clause, part of the week is considered as a full week. In case of delayed delivery, the Director, IIT Guwahati reserves the right not to accept the subject consignment.

- **18. Enquiry during the course of evaluation not allowed:** No enquiry shall be made by the bidder(s) during the course of evaluation of the tender till final decision is conveyed to the successful bidder(s). However, the Purchase Committee or its authorized representative (IIT Guwahati) can make any enquiry/seek clarification from the bidders. In such a situation, the agency shall extend full co-operation. The bidders may also be asked to arrange demonstration of the offered items, in a short period notice, as such the bidders have to be ready for the same.
- **19. Acceptance of quotations:** The acceptance of the quotation will rest solely with the Director, IITG, who in the interest of the Institute is not bound to accept the lowest quotation and reserves the right to himself to reject or partially accept any or all the quotations received without assigning any reasons.

- **20. Force Majeure:** If the performance of the obligation of either party is rendered commercially impossible by any of the events hereafter mentioned that party shall be under no obligation to perform the agreement under order after giving notice of 15 days from the date of such an event in writing to the other party, and the events referred to are as follows:
- I) Any law, statute or ordinance, order action or regulations of the Government of India,
- II) Any kind of natural disaster, and
- III) Strikes acts of the Public enemy, war, insurrections, riots, lockouts, sabotage.
- **21. Termination for default:** Default is said to have occurred
- (a) If the equipment or any of its component is found having poor workmanship, faulty designs, poor performance and bad quality of materials used.
- (b) If the supplier fails to deliver any or all of the services within the time period(s) specified in the purchase order or any extension thereof granted by IIT.
- (c) If the supplier fails to perform any other obligation(s) under the contract.
- (d) Under the above circumstances IIT may terminate the contract / purchase order in whole or in part and forfeit the EMD/PBG as applicable. In addition to above, IIT may at its discretion also take the following actions: IIT may procure, upon such terms and in such manner, as it deems appropriate, goods similar to the undelivered items/products and the defaulting supplier shall be liable to compensate IIT for any extra expenditure involved towards goods and services obtained. Besides, the Director, IITG, reserves the right to impose any other form of penalty as deemed fit including blacklisting of the vendor.

22. Applicable Law:

- (a) The contract shall be governed by the laws and procedures established by Govt. of India and subject to exclusive jurisdiction of Competent Court and Forum in Guwahati or India only.
- (b) Any dispute arising out of this purchase shall be referred to the Director IIT Guwahati, and if either of the parties hereto is dissatisfied with the decision, the dispute shall be referred to the decision of an Arbitrator, who should be acceptable to both the parties, to be appointed by the Director of the Institute. The decision of such Arbitrator shall be final and binding on both the parties.
- **23.** Additional terms for imported goods: Following terms besides the aforementioned terms (1 to 22 except 14 and 16) will be applicable in case of foreign supply:
- a) Rates: Rates quoted should be on Delivered Duty Paid (DDP)/ Delivery at Place (DAP) basis, with break-ups as per details given in the BOQ. Failing to provide as per BOQ, quotation may not be accepted.
- b) Exchange rate: Rate of foreign exchange shall be the rate prevailing on the date of quotation opening.
- c) Delivery: Maximum within 90 days from the date of issue/receipt of purchase order.
- (i) Door Delivery till the site of location at Department of Electronics and Electrical Engineering) IIT Guwahati.
 - (ii) While transshipment will be allowed, part shipment will not be allowed.

d) Payment:

- (i) Above INR 5 Lacs by Letter of Credit (LOC): By an irrevocable letter of Credit at DDP/DAP IITG value negotiable through any overseas branch of Axis Bank with unrestricted provision. 90% of payment will be released on receipt of the shipping document and balance 10% after receipt of consignment. LOC will be established on receipt of Order acknowledgment and Performance Bank Guarantee (PBG)
- (ii) Below INR 5 Lacs by Foreign Demand Draft (FDD)/Wire Transfer as given below: 100% payment will be released against receipt of Order Acknowledgment and Proforma Invoice
- **e) Agency Commission:** The percentage of ex-works value to be paid to Indian agent in equivalent Indian currency as agency commission as applicable will have to be clearly stated in the quotation.
- f) Country of Origin: While Country of Origin will have to be stated in the Original Invoice for payment through LoC.
- **g)** LoC Amendment: LoC/FDD amendment charges due to mistake on the part of the supplier, if any, will have to be borne by the supplier.

(Manash Jyoti Pathak)
Assistant Registrar
Research & Development Section

ANNEXURE - I

TECHNICAL SPECIFICATIONS

Refⁿ: IITG/RND/T118/23-24 Date: 21.11.2023

1. Generic Name of Equipment

Virtex FPGA Evaluation Kit and NVIDIA Jetson AGX Orin Developer Kit



VIRTEX-7 FPGA VC707 EVALUATION KIT

FULL-FEATURED, HIGHEST-PERFORMANCE FPGA DESIGN PLATFORM

VIRTEX-7 FPGA VC707 EVALUATION KIT: HIGHLY FLEXIBLE BASE PLATFORM FOR SIMPLIFYING TECHNOLOGY EVALUATIONS AND ACCELERATING DESIGNS

D Design Challenges

- Consumer expectations and higher traffic levels are driving insatiable bandwidth demands
- Power and performance are becoming the primary concerns for most designs
- ASIC/ASSP designs are increasingly complex (multi-chip barriers)
- Engineers are being pushed to do more (innovation, differentiation) with less (time, budget, and resources)

D Xilinx Solutions

- Virtex-7 FPGA family and 28rm leadership: new benchmarks for performance (logic density, I/O bandwidth, and signal processing) and 50% power savings
- Faster start up, with integrated silicon, software, and IP and complete documentation
- Rapid evaluations with base reference design and other pre-verified examples that exercise device and board features
- Convenient, easy-to-use GUI displays combined results from different implementations

The Xilinx® Virtex®-7 FPGA VC707 Evaluation Kit gives designers an easy starting point for evaluating and leveraging devices that deliver breakthrough performance, capacity, and power efficiency. Out of the box, this platform speeds time to market for the full-range of Virtex-7 FPGA-based applications including advanced systems for wired and wireless communications, aerospace and defense, medical, and broadcasting markets. The highly floxible kit combines fully integrated hardware, software, and IP with pre-verified reference designs that maximize productivity and let designers immediately focus on their unique project requirements.

Fast Access to Virtex-7 Capacity FPGA and Performance

As a base level Targeted Design Platform, this kit provides a flexible environment for designs that need to implement a DDR3 memory interface, 10Gigabit Ethernet, PCI Express*, Analog Mixed Signal (AMS) capabilities, and other high-speed serial connectivity. Based on the Virtex-7 VX485T-2 FPGA, the kit is the optimal choice for advanced systems that need the highest performance and highest bandwidth connectivity. It speeds the development of designs that can leverage the full breadth of the Virtex-7 product families, all of which offer maximum power efficiency—requiring 50% less power than previous generation FPGAs.

Boosted Productivity and Shorter Time to Innovation

The combination of the Xilinx ISE® Design Suite Logic software and a base reference design simplifies access to the Virtex-7 features and built-in board capabilities. The source code can be scaled, to adjust the design for each target application. The easy-to-use base reference design interface presents evaluation results in a convenient, intuitive manner, and shortens the learning curve for a smooth migration to the 7 series. A variety of additional designs and demonstrations are included to save time and accelerate development for a broad range of Virtex-7 FPGA-based applications.



7 SERIES FPGAS

What's Inside the VC707 Evaluation Kit

- VC707 evaluation board with the Vintex-7 XC7VX485T-2FFG1761CES FPGA
- Full-seat ISE Design Suite Logic Edition, device-locked for the Virtex-7 XC7VX485T-2FFG1761CES FPGA
- · Reference and example designs and demonstrations*
- Board design files*
- Documentation*, including a step-by-step Getting Started Guide
- · USB cables, Ethernet cable, and universal power supply

Reference Designs and Demonstrations*

- · Base Reference Design (scalable) and Demonstration
- · PCI Express x8 Gen2 Design
- DDR3 Memory Interface Design
- · Gigabit Ethernet Design
- 10Gigabit Ethernet Design
- · BIST Board Diagnostic Design (built-in self test, with status GUI)
- IBERT XCVR Test Design

*Designs, demonstrations, and documentation are delivered on a USB

VIRTEX-7 FPGA VC707 EVALUATION KIT



Take the NEXT STEP

For more information, support, documents, and reference designs, or to purchase, please visit: www.xillinx.com/vc707

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Printed in the U.S.A. PN 2409-1



Develop the next generation of autonomous machines.

The NVIDIA® Jetson AGX Orin® Developer Kit makes it easy to get started with the Jetson AGX Orin module. Compact size, lots of connectors, and up to 275 TOPS of Al performance make this developer kit perfect for prototyping advanced Al-powered robots and other autonomous machines.

Jetson AGX Orin features an NVIDIA Ampere architecture GPU together with nextgeneration deep learning and vision accelerators, and its high-speed IO and fast memory bandwidth can feed multiple concurrent Al application pipelines. This means you can develop solutions using your largest and most complex Al models to solve problems such as natural language understanding, 3D perception, and multisensor fusion.

The NVIDIA JetPack SDK brings the NVIDIA AI software stack to Jetson, along with application development and optimization tools. Software for specific use cases is available, including NVIDIA Isaac* for robotics and Metropolis for smart cities. Plus, you can save significant time developing your Al solution when you use your datasets with NVIDIA TAO Toolkit to fine-tune pretrained AI models from the NGC" catalog.

Jetson ecosystem partners offer additional Al and system software, developer tools, and custom software development. They can also help with cameras and other sensors, as well as carrier boards and design services for your product.

With the computing capability of more than eight Jetson AGX Xavier* systems in a developer kit that integrates the latest NVIDIA GPU technology with the world's most advanced deep learning software stack, you'll have the flexibility to create tomorrow's Al solutions, as well as today's.

Key Features

Developer kit contents

- Jetson AGX Orin module with heat sink and reference carrier board
- > 802.11ac/abgn wireless Network Interface Controller
- Power adapter and USB Type-C cord
- ► USB Type-C to USB Type-A cord
- Quick Start and Support Guide

Jetson AGX Orth module

- > 2048-core NVIDIA Ampere architecture GPU with 64 Tensor cores
- > 12-core Arm* Cortex*-A78AE v8.2 64-bit CPU
- 2x NVDLA v2.0
- PVA v2.0
- 32GB 256-bit LPDDR5
- 64GB eMMC 5.1

Reference carrier board

- MIPI CSI-2 carriera connector
- PCle x16 connector (supports xfl)
- (ligabit Ethernet
- M.2 Key E, M.2. Key M
- > 2x USB Type-C
- + 4x USB 3.2 Type-A
- Micro-USB 2.0 ffor Device Model
- DisplayPort Dual-Mode
- microSD slot
- > 40-pin header [GPIOs, I2C, I2S, SPI, UAR1]
- Power, Force Recovery, and Reset butt



NVIDIA JETSON AGX ORIN DEVELOPER KIT TECHNICAL SPECIFICATIONS

JETSON AGX ORIN MODULE

GPU NVIDIA Ampere architecture with 2048 NVIDIA® CUDA® cores an 64 Tensor cores							
CPU	12-core Arm Cortex-A78AE v8.2 64-bit CPU 3MB L2 + 6MB L3						
DL Accelerator	2x NVDLA v2.0						
Vision Accelerator	PVA V2.0						
Memory	32GB 254-bit LPDDR5 204.8GB/s						
Storage	64GB eMMC 5.1						
Video Encode	2x 4K60 4x 4K30 8x 1080p60 16x 1080p30 (H.265)						
Video Decode	1x 8K30 3x 4K60 6x 4K30 12x 1080p60 24x 1080p30 (H.265)						
Power	15W-60W						

Refer to the Software Features section of the Laesa MMERA Jeson Linux Developer Guide for a list of supported features

REFERENCE CARRIER BOARD

Camera	16 Iane MIPI CSI-2 connector
PCle	x16 PCIe slot supporting: x8 PCIe Gen4
RJ45	Up to 10GbE
M.2 Key M	x4 PCIe Gen 4
M.2 Key E	x1 PCle Gen 4, USB 2.0, UART, I2S
USB Type-C	2x USB 3.2 Gen2
USB Type-A	2x USB 3.2 Gen2, 2x USB 3.2 Gen1
USB Micro-B	USB 2.0
DisplayPort	DisplayPort 1.4a (+MST)
microSD slot	UHS-1 cards up to SDR104 mode
Other	40-pin header (I2C, GPIO, SPI, CAN, I2S, UART, DMIC) 12-pin audio panel header 10-pin JTAG header 4-pin fan header 2-pin RTC battery backup connector DC power Jack Power, Force Recovery, and Reset buttons
Dimensions	110mm x 110mm x 71.65mm (Height includes feet, carrier board, module, and thermal solution

Learn more

Learn more at www.nvidia.com/JotsonAGXDri

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Your Ref No. IITG/RND/T118/23-24

ANNEXURE -II COMPLIANCE CERTIFICATE

Refⁿ: IITG/RND/T118/23-24 Date: 21.11.2023

Certify that we have carefully examined the NIQ terms and fully understood its implications and do hereby agree to comply with all the terms, and hereby submit this compliance certificate.

SI. No.	General Terms and Conditions	Yes / No
01	Details of the technical features of the offered equipment vis-à-vis NIQ specification	
02	Rates quoted as per instruction	
03	Standard Technical literature on each of the items offered	
04	Warranty period agreed	
05	Valid ISO provided	
06	Validity period of quoted rate agreed	
07	EMD submitted (as applicable)	
08	PBG term agreed	
09	Delivery terms agreed	
10	Certificate of Up-to-date Sales Tax clearance, GST Registration Certificate provided	
11	Payment term agreed	
12	Penalty clause for delay agreed	
13	Literature- Printed Literature with highlight and tag on the quoted item provided	
14	Manufacturer/Authorization certificate submitted	
15	Quality certificate as claimed submitted	
16	After Sales Service term agreed and detail address & contact provided	
17	Not blacklisted by any Govt. Organizations/Institutions	

Sign.	:.													
Vendor:	N	1	S	ì.		 						 	 	

Official seal of the vendor

Your Ref No.: IITG/RND/ T118/23-24

ANNEXURE-III BIDDERS DETAIL

Refⁿ: IITG/RND/ T118/23-24 Date: 21.11.2023

SI. No.	Name of the company/Firm	To be filled by vendor (Documentary Proof must be attached as applicable)
01	Registered office Name & Address Details of contact person Name designation Telephone number e-mail	
02	Name & Address of service centre in Guwahati city Contact person/s Name designation Telephone number e-mail Details with contact no. of staff who will be involved in this project.	
03	Is the company/firm a registered company/firm? If yes, mention year and place of the establishment of the company and submit documentary proof.	
04	Is the company/firm registered for GST? If yes, submit valid registration certificate.	
05	List the major clients with whom your organization has been associated and submit documentary proof/PO.	

Note: Document/s if attached to the NIQ compliance Certificate the same may be referred to in the remark column above.

Sign.	:			 			 			 				
Vendor:	M	/	3.	 	 				 					

Official seal of the vendor

ANNEXURE -IV

Your Ref. No: IITG/RND/T118/23-24

INDIAN INSTITUTE OF TECHNOLOGY GUWAHATI

BANK GUARANTEE FORM FOR EMD

In consideration of the Registrar (hereinafter called the "Tender/NIQ Inviting Authority" or "Client") has floated tender/NIQ no(hereinafter called "said tender"), for Supply and Installation of as per terms and conditions mentioned in the above tender/ NIQ, M/s
as per terms and conditions mentioned in the above tender ring, wis
process and agreed to production of an irrevocable bank guarantee for Rs (Rupees
only) as an Earnest Money Deposit (EMD) towards compliance of its obligations in
accordance with the terms and conditions in the said tender.
We (hereafter referred to as the "Bank") hereby undertake following: 1. We undertake to pay to the Client any money so demanded not withstanding any dispute or disputes raised by the
tenderer(s) in any suit or proceeding pending before any Court or Tribunal relating there to, our liability under this present
being absolute and unequivocal. The payment so made by us under bond shall be a valid discharge of our liability for
payment there under, and the tenderer(s) shall have no claim against us for making such payment.
2. We further agree that the Guarantee herein contained shall (indicate the name of the Bank) remain in full force and
effect during the period that would be taken for the performance of the said tender, and it shall continue to be enforceable till
all the dues of the Client under or by virtue of the said tender have been fully paid, and its claims satisfied or discharged, or
till the Client certifies that the terms & conditions of the said tender have been fully and properly carried out by the said
tenderer(s), and accordingly discharges this guarantee.
3. We further agree with the Client that the Client shall have the fullest liberty without our consent, and without effecting in
any manner our obligations hereunder, to vary any of the terms & conditions of the said tender or to extend time of performance by the said tenderer(s) from time to time or to postpone for any time or from time to time any of the powers
exercisable by the Client against the said tenderer(s) and to forbear or enforce any of the terms and conditions relating to the
said agreement, and we shall not be relieved from our liability by reason of any such variation or extension being granted to
the said tenderer(s) or for any forbearance, act of omission on the part of the Client or any indulgence by the Client to the
said tenderer(s) or by any such matter or thing whatsoever which under the law relating to sureties would, but for this
provision, have effect of so relieving us.
4. This Guarantee will not be discharged due to the change in the constitution of the Bank or the tenderer(s).
5. We lastly undertake not to revoke this Guarantee except with the prior consent of the Client in writing.
6. This guarantee shall be valid up to unless extended on demand by the Client Notwithstanding anything
mentioned above, our liability against this Guarantee is restricted to Rs (Rupees
only) and unless a claim in writing is lodged with us under this Guarantee shall stand discharged.
(Signature of the authorized afficer of the Bank)
(Signature of the authorized officer of the Bank)
Name and designation of the officer
Seal, name & address of the Bank and address of the Branch

Your Ref. No: IITG/RND/T118/23-24

ANNEXURE-V

PERFORMANCE BANK GUARANTEE

To:	
The Registrar, Indian Institute of Technology, Guwahati- 781 039	
WHEREAS (Name o has undertaken, in pursuance of Contract No:, da	ated: 20 to supply
AND WHEREAS it has been stipulated by you in the said order. Bank Guarantee by a recognized bank for the sum specified Supplier's performance obligations in accordance with the order.	
AND WHEREAS we have agreed to give the Supplier a Guarante THEREFORE, WE hereby affirm that we are Guarantors and re up to a total of	esponsible to you, on behalf of the Supplier, (Amount of the Guarantee in Words and emand declaring the Supplier to be in default ums within the limit of
This guarantee is valid until theday of	
Signature and Seal	of Guarantors
	Date20 Address:

All correspondence with reference to this guarantee shall be made at the following address:

The Assistant Registrar Research & Development Section Indian Institute of Technology Guwahati- 781 039, Assam