



Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for a **Walk-in interview for Ad-hoc recruitment** for the following post(s) in the project entitled, "**Developing hardware and software post-quantum secure platforms for performing homomorphic computations on encrypted data**" at the Department of Electronics & Electrical Engineering, IIT Guwahati.

Date: 1st August 2025 (Friday)

Time: 12.00 pm (noon)

Venue: Walk-in Interview (Conference Room, EICT Academy, Technology Complex)

Sl. No.	Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA (Rs.)	Medical (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	Sr. Project Scientist	02	75000	13500	1250	89750.00	6	PhD degree + 3 yrs exp.or Masters degree in EEE/ECE/CSE/Mathematics + 6 yrs exp.
2	Project Scientist	02	65500	11790	1250	78540.00	6	PhD degree + 3 yrs exp.or Masters degree in EEE/ECE/CSE/Mathematics + 6 yrs exp.
3	Project Engineer	02	60000	10800	1250	72050.00	6	PhD degree Or Master's degree in Engineering/Design + 3 yrs exp. Or Bachelor's degree in Engineering/Design + 6 yrs exp.

How to apply and selection process: Candidates have to appear in a Walk-in Interview, which is scheduled on **1st August 2025 (Friday)** at 12:00 noon. Please apply through the online application form with your Advance copy of CV giving details of all educational qualifications, experience, contact address, phone no., email etc. along with scan copies of relevant documents on or before 28th July 2025.

Application link: <https://forms.gle/2tU9wu7jt1v5mPXF6>

Selection will be based on the performance of the candidate in the Walk-in interview. Candidates will not be sent any call letter separately.

For any clarification, contact: Dr Srinivasan Krishnaswamy (Principal Investigator)

Email: srinikris@iitg.ac.in

Phone: 0361-258-2526

No campus accommodation will be available for the selected candidates.

No TA/DA will be paid to the candidates for appearing in the test and interview.

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Assistant Registrar (R&D)