



Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for a **Walk-in interview** for the following post(s) in the project entitled, "**Design and fabrication of 2D photonic crystal devices for data center applications**" at the department of Electronics and Electrical Engineering, IIT Guwahati.

Date: 31 Mar 2023 (Friday)

Time: 1130

Venue: online & Offline

Sl. No.	Project Staff Designation	Number of Vacancies	Pay Recommended (₹)	HRA (₹)	Medical (₹)	Total Amount (₹)	Duration of Appointment in months	Qualifications
1	JRF (GATE)	1	31000/-	4960/-	1250/-	37210/-	7 months and extendable based on performance	1. B.Tech in EE / ECE / EEE / EIE or equivalent with valid GATE score in EE or EC ONLY. 2. M.Tech in VLSI / Photonics/ Microelectronics in related area can also apply. 3. Should have basic understanding of semiconductor devices and optoelectronic devices. 4. Knowledge in numerical tools like Synopsys, SILVACO TCAD, OptiWAVE, LUMERICAL, MATLAB, LabVIEW etc. is preferable..

How to apply and selection process:

Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the time of interview on 31 Mar 2023 (Friday) at 1130. Venue: online & Offline. Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV must be sent to the Principal Investigator on or before 30th March 2023(1700Hrs).

For any clarification, contact:

Ramesh Kumar Sonkar (Principal Investigator)

Email: sonkar@iitg.ac.in

Phone: 03612582535

No campus accommodation will be available for the selected candidates. No TA/DA will be paid to the candidates for appearing in the test and interview.