



Indian Institute of Technology Guwahati

Research and Development Section

Guwahati-781039, Assam

Applications are invited for an **ONLINE** interview for the following post(s) in the project entitled, "**ML4EDA: Next Generation AI-Assisted Chip Design**" at the Department of Computer Science and Engineering, IIT Guwahati.

Date: 6th July 2026 (Monday)

Time: 10.00 am

Venue: ONLINE

| Sl. No. | Designation | Number of Vacancies | Pay Recommended (Rs.) | HRA (Rs.) | Medical (Rs.) | Total Amount (Rs.) | Duration of Appointment in months | Qualifications |
|---------|------------------------|---------------------|-----------------------|-----------|---------------|--------------------|-----------------------------------|--|
| 1 | Research Associate (1) | 01 | 58000 | 11600 | 1250 | 70850.00 | 6 | <p>Ph.D. degree in a relevant area, or M.Tech./M.E. (or equivalent) with at least 3 years of research, in Computer Science and Engineering (CSE), Electronics and Communication Engineering (ECE), or a related discipline.</p> <ul style="list-style-type: none">• Experience in Electronic Design Automation (EDA) tools and methodologies.• Familiarity with open-source EDA tools such as ABC, Yosys, OpenROAD, and related frameworks.• Knowledge of Machine Learning (ML) techniques and their application to EDA problems.• Strong programming and problem-solving skills are desirable.• Prior research or project experience in VLSI CAD, hardware design, formal verification, or ML for EDA will be an added advantage. |

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|--|-----------------------|----|-------|------|------|-------|---|--|
| | Project Associate (1) | 01 | 37000 | 7400 | 1250 | 45650 | 6 | <ul style="list-style-type: none"> • B.E./B.Tech. in Computer Science and Engineering (CSE), Electronics and Communication Engineering (ECE), or a related discipline. • Experience in Electronic Design Automation (EDA) tools and methodologies. • Familiarity with open-source EDA tools such as ABC, Yosys, OpenROAD, and related frameworks. • Knowledge of Machine Learning (ML) techniques and their application to EDA problems. • Strong programming and problem-solving skills are desirable. • Prior research or project experience in VLSI CAD or ML for EDA will be an added advantage. |
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Candidates are also required to appear for an ONLINE interview scheduled on **6th July 2026 (Monday) at 10:00 AM.**

Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. **However, only shortlisted candidates will get the meeting link from the PI.**

For any clarification, contact: Chandan Karfa (Principal Investigator)

Email: ckarfa@iitg.ac.in

Phone: 03612582375

How to apply and selection process:

Interested and eligible candidates should apply online by visiting the Recruitment Portal of Research & Development Cell, IIT Guwahati (<https://iitg.ac.in/rndproj/recruitment/>).

Applicants must carefully follow the instructions and guidelines provided on the portal. The relevant position can be accessed by searching for the project title as mentioned in this advertisement.

For any technical assistance related to the online application process, candidates may email to ernd@iitg.ac.in.