International Workshop on Intelligent Multi-Core Systems
22nd-24th February 2022, IIT Guwahati

SPARC
Scheme for Promotion of Academic and Research Collaboration

(Under the SPARC Project 'Approximate Computing Techniques for Resource Constrained Edge Devices')

Resource Person:
Dr. Amit Singh
Associate Professor
School of Computer Science and Electronic Engineering
University of Essex, UK

Workshop will be conducted in online mode through MS Teams.
Dates: 22nd – 24th February, 2022 (3 days, 1.5 hrs./day)
Time: 12:30 PM – 02:00 PM (IST)
[3 days, 1.5 hours per day]

Workshop Contents:
- **Overview of multi-core systems**
- **Computation and communication issues in multi-core systems**
- **The need for intelligence**
- **Intelligent adaptations and challenges**
- **The search for the right level of intelligence**
- **State-of-the-art intelligence**
- **Envisioned future intelligence**

Resource Person Profile: [https://www.essex.ac.uk/people/singh42308/amit-singh]

Dr Amit Kumar Singh is an Associate Professor at the University of Essex, UK. He received the B.Tech. degree in Electronics Engineering from Indian Institute of Technology (Indian School of Mines), Dhanbad, India, in 2006, and the Ph.D. degree from the School of Computer Engineering, Nanyang Technological University (NTU), Singapore, in 2013. He was with HCL Technologies, India for a year and a half until 2008. He has post-doctoral research experience for over five years at several reputed universities. His current research interests are the design and optimization of multi-core-based computing systems with focus on performance, energy, temperature, reliability and security. He has published over 110 papers in reputed journals/conferences, and received several best paper awards, e.g., IEEE TC February 2018 Featured Paper, ICCES 2017, ISORC 2016, PDP 2015, HiPEAC 2013 and GLSVLSI 2014 runner up. He has served on the TPC of IEEE/ACM conferences like DAC, DATE, ICCAD, CASES and CODES+ISSS.

For more details, visit the workshop website:https://www.iitg.ac.in/johnjose/sparc5.html

Eligibility Criteria
(1) UG, PG, and Ph.D. students & faculty of CSE/ECE/EEE/IT branches.
(2) Personnel from industry and R&D firms who are interested in this domain.

No Registration Fee || Registration Form: https://tinyurl.com/2p86un2w
Last date for registration   : 17.02.2022
Publication of provisional list of selected candidates on the workshop website : 18.02.2022
(No email will be sent regarding shortlisting)
E-certificates will be provided to participants who attend all sessions of the workshop.

Programme Coordinators:
Dr. John Jose & Dr. T. Venkatesh
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