



International Workshop on Scheme for Promotion of Academica Blockchain Technologies and Decentralized Environments

(22th July & 29th July, 2021)

(Under the SPARC Project 'Approximate Computing Techniques for Resource Constrained Edge Devices')

Resource Person:

Dr. Davide Patti Electric, Electronics and Computer Engineering Department University of Catania, Italy

Workshop will be conducted in online mode through MS Teams

Dates: 22th July, 2021 and 29th July, 2021 (Thursdays) Time: 3:00 PM to 4:30 PM & 5:00 to 6:30 PM (IST)

[2 days, 3 hours per day, total 6 hours]

Workshop Contents

- Physical vs Digital assets: the double spending problem, Exchange of value in human history
- Motivations for an open blockchain, Hash Functions and Asymmetric Cryptography
- Proof-of-work in Bitcoin, Difficulty Adjustment, Mining, Blocks and Transactions
- Tools visualizing and exploring the blockchain, Bitcoin as a Platform: the Lightning Network
- Bitcoin and altcoins: the trilemma, Ethereum and Smart Contracts
- Blockchain and IoT, Proof-of-existence, NFT

Resource Person Profile

Dr. Davide Patti is currently an Assistant Professor at the Electric, Electronics, and Computer Engineering Department, University of Catania. He has published several works on Design Space Exploration, Low-power techniques for Embedded Systems, and Network-on-Chip architectures. He has been teaching several courses in the Computer Science field, including Human-Computer Interaction at the Faculty of Psychology of Kore University, Enna, Italy and lectures at the University of Southampton, UK. He is a Steering Committee member of International Workshop on Network on Chip Architectures (NoCArc). He is also Associate Editor of IEEE Access and Guest Editor of many special issues. He is co-author of the Noxim project, currently used by a vast community of researchers working in the area of on-chip communication systems and employed as an experimental platform in more than 500 scientific papers published in refereed ACM and IEEE international conferences and journals. [http://utenti.dieei.unict.it/users/dpatti]

For more details, visit the workshop website: https://www.iitg.ac.in/johnjose/sparc4.html

Eligibility Criteria

- (1) UG, PG and Ph.D students & faculty computer science, electronics and related area.
- (2) Personnel from industry and R&D firms who are interested in this domain.

No Registration Fee | Registration Form: https://tinyurl.com/ygo42jtu

Last date for registration : 17.07.2021

Publication of provisional list of selected candidates on workshop website

Last date for confirmation by shortlisted candidates : 20.07.2021

Sharing of MS Teams links and other guidelines to confirmed candidates : 21.07.2021

E-certificates will be provided to participants who attend all sessions of the workshop.

Programme Coordinators:

Dr. John Jose & Dr. T. Venkatesh

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