

DEITY Sponsored Workshop on
Electronic System Design and Manufacturing

March 16, 2015

Indian Institute of Technology Guwahati

Program Schedule

SL. No	Event	Time
1	Registration & Inauguration	09.00 am - 10.30 am
2	Tea	10.30 am - 11.00 am
3	Full Custom IC Design for ESDM by Dr. Vijay Nath	11.00 am-12.00 pm
4	Solar Photovoltaic Array Performance Analyzer by Dr. Gaurav Trivedi	12.00 pm - 1.00 pm
5	Lunch	1.00 pm - 2.00 pm
6	VLSI aspect of Signal Processing by Dr. Shaik Rafi Ahamed	2.00 pm – 3.00 pm
7	CAD for Digital VLSI: From Concept to Chip by Dr. Arnab Sarkar	3.00 pm – 4.00 pm
8	Tea, vote of thanks and distribution of certificates	4.00 pm – 5.00 pm

For any query regarding program schedule, please contact:

Dr. Hanumant Singh Shekhawat

Assistant Professor

Department of EEE

IIT Guwahati

Email: h.s.shekhawat@iitg.ernet.in