

Course Structure: M.Tech. (VLSI)

Semester I			
Code	Course Name	L-T-P	Credits
EE 510	Semiconductor Device Modeling	3-0-0	6
EE 511	Semiconductor IC Technology	3-0-0	6
EE 513	VLSI Lab I	0-0-4	4
EE 514	Digital IC Design	3-0-0	6
EE 6/7xx	Elective I	3-0-0	6
EE 6/7xx	Elective II	3-0-0	6
	Total credits	15-0-4	34

Semester II			
Code	Course Name	L-T-P	Credits
EE 512	Analog IC Design	3-0-0	6
EE 515	VLSI System Design	3-0-0	6
EE 516	VLSI DSP	3-0-0	6
EE 517	VLSI Lab II	0-0-4	4
EE 518	VLSI Lab III	0-0-4	4
EE 6/7xx	Elective III	3-0-0	6
	Total credits	12-0-8	32

Semester III			
Code	Course Name	L-T-P	Credits
EE 698	Project Phase-I	0-0-24	24

Semester IV			
Code	Course Name	L-T-P	Credits
EE 699	Project Phase-II	0-0-24	24