



Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for a **Walk in interview** for the following post(s) in the project entitled, "**SMDP - C2SD**" at the department of Electronics and Electrical Engineering, IIT Guwahati.

Date: 07 Dec 2018 (Friday)

Time: 10 AM

Venue: EEE Conference Room

Sl. No.	Project Staff Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA Required (Rs.)	Medical Required (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	Assistant Project Engineer	02	23000	Yes Rs. 4600	1000	28600.00	11	Bachelor's degree in Engineering (VLSI area Experience preferred)
2	Assistant Project Engineer	01	23000	Yes Rs. 4600	1000	28600.00	11	Bachelor's degree in Engineering (MTech in VLSI or related Experience preferred)

How to apply and selection process: Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the time of interview on 07 Dec 2018 (Friday) at 10 AM. Venue: EEE Conference Room. Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV may be sent to the Principal Investigator.

For any clarification, contact: Roy Paily Palathinkal (Principal Investigator)

Email: roypaily@iitg.ac.in

Phone: 9435548082

No TA/DA will be paid to the candidates for appearing in the test and interview.

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Asstt. Registrar(R&D)

