



## Research and Development Section Indian Institute of Technology Guwahati Guwahati-781039, Assam

Applications are invited for a **Walk in interview** for the following post(s) in the project entitled, "**Development of high power density solid state transformer using direct AC to AC power electronic conversion**" at the department of Electronics and Electrical Engineering, IIT Guwahati.

**Date: 26 Oct 2018 (Friday)**

**Time: 9:00 am**

**Venue: Conference room, Dept. of EEE**

Sl. No.	Project Staff Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA Required (Rs.)	Medical Required (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	JRF (GATE)	1	25000	Yes Rs. 5000	1000	31000.00	9	B.E./B.Tech in Electrical/Electronics & Communication Engg. Must have qualified GATE in EE/EC at least once. Desirable to have knowledge in electrical circuits and power electronics.

**How to apply and selection process:** Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the time of interview on 26 Oct 2018 (Friday) at 9:00 am. Venue: Conference room, Dept. of EEE. Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV may be sent to the Principal Investigator.

For any clarification, contact: Shabari Nath (Principal Investigator)

Email: [snath@iitg.ac.in](mailto:snath@iitg.ac.in)

Phone: 03612583463

**No TA/DA will be paid to the candidates for appearing in the test and interview.**

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**Asstt. Registrar(R&D)**