



Research and Development Section  
Indian Institute of Technology Guwahati  
Guwahati-781039, Assam

Applications are invited for a **Walk in interview** for the following post(s) in the project entitled, "**Development of ESD I/O Pads for CIS/CCD Image Sensor for 0.18um SCL Foundry**" at the department of Electronics and Electrical Engineering, IIT Guwahati.

**Date: 15 Sep 2017 (Friday)**

**Time: 10:00AM**

**Venue: EEE Conference Room C1**

Sl. No.	Project Staff Designation	Number of Vacancies	Pay Recommended (Rs.)	HRA Required (Rs.)	Medical Required (Rs.)	Total Amount (Rs.)	Duration of Appointment in months	Qualifications
1	Research Associate (1)	1	36000	Yes Rs. 7200	1000	44200.00	11	Ph.D/M.Tech with two years experience

**How to apply and selection process:** Candidates have to appear in the Walk in Interview along with an application/CV on plain paper giving details of all educational qualifications, experience, contact address, phone no., E - mail etc. and submit photocopies of relevant documents at the time of interview on 15 Sep 2017 (Friday) at 10:00AM. Venue: EEE Conference Room C1. Selection will be based on the performance of the candidate in the interview. Candidates will not be sent any call letter separately. Advance copy of CV may be sent to the Principal Investigator.

For any clarification, contact: Gaurav Trivedi (Principal Investigator)

Email: [trivedi@iitg.ernet.in](mailto:trivedi@iitg.ernet.in)

Phone: 9435582802

**No TA/DA will be paid to the candidates for appearing in the test and interview.**

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Dy. Registrar(R&D)