Analog & Digital Electronics Course No: PH-218

Lec-19: Power amplifiers and Output Stages

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Power Amplifiers

A Power amplifier is large signal amplifier and this is generally a last stage of a multistage amplifier.

> The function of a practical power amplifier is to amplify a weak signal until sufficient power is achieved to operate a loudspeaker or output device.

> Typical output power rating of a power amplifier will be 1W or higher. The schematic diagram of a practical power amplifier is shown below –



> The driver stage operates as a class A power amplifier and supplies the drive for the output stage.

The last output stage is essentially a power amplifier and its purpose is to transfer maximum power to the output device (speaker). The output stage generally employ class B amplifiers in push-pull arrangement.

Power Amplifiers

A large signal amplifier means much larger portion of load line is used during signal operation compared to small signal amplifier.

> A small signal amplifier (handle ac signal <10mV) operate over a linear portion of load line.

>In case of power amplifier, we can not use small signal approximation directly to calculate voltage gain, current gain and input/output impedance.

> Ideal power amplifier will deliver 100% of the power it draws from the supply to load. In practice, this can never occur.

> The reason for this is the fact that the components in the amplifier will all dissipate some of the power that is being drawn form the supply.

Performance parameters of power amplifier

Amplifier Efficiency : A figure of merit for the power amplifier is its efficiency

> It is defined as a ratio of output ac power to the input dc power.

$$\eta = \frac{ac \text{ output power}}{dc \text{ input power}} \times 100\% = \frac{P_o(ac)}{P_i(dc)} \times 100\%$$

Distortion

> The change in output wave shape from the input wave shape of an amplifier is known as distortion.

The distortion can be reduced using negative feedback in amplifier.

Power dissipation capability

> The ability of a power amplifier to dissipate heat is known as power dissipation capability.

 \succ To achieve better heat dissipation heat sink (metal case) is attached with power transistor. The increase surface area allows heat to escape easily.

AC Load line

The ac load line of a given amplifier will not follow the plot of the dc load line.

This is due to the dc load of an amplifier is different from the ac load.



the ac load line will tell you the maximum possible peak-to-peak output voltage (V_{pp}) from a given amplifier.

- When an ac signal is applied to the base of the transistor, I_c and V_{ce} will both vary around their Q-point values.
- > When the *Q*-point is centered, I_c and V_{CE} can both make the maximum possible transitions above and below their initial dc values.



AC Load line







(a) Limited by saturation

(b) Limited by cutoff

When the *Q*-point is below midpoint on the load line, the input signal may cause the transistor to cutoff. This can also cause a portion of the output signal to be clipped.

When the *Q*-point is above the center on the load line, the input signal may cause the transistor to saturate. When this happens, a part of the output signal will be *clipped* off.



Output Stages: Power amplifier

> Output stages are classified according to the collector current waveform that results when an input signal is applied.



Class A Power amplifier

> If the collector current flows at all times during the full cycle of the signal, the power amplifier is known as class A amplifier.





> With class A amplifier Q point lies middle of the load line so that signal can swing over the maximum possible range without saturating or cut off the transistor.

Efficiency: RC coupled Class A Power amplifier

The total dc power, $P_i(dc)$, that an amplifier draws from the power supply :

$$P_{i}(dc) = V_{cc}I_{cc} \qquad I_{cc} = I_{cQ} + I_{1}$$
$$I_{cc} \approx I_{cQ} \quad (I_{cQ} >> I_{1})$$
$$P_{i}(dc) = V_{cc}I_{cQ}$$
$$P_{o}(ac) = V_{ce} \times I_{ce}$$

 V_{ce} and I_{ce} represents rms value of the signal.

$$V_{ce} = \frac{V_{ce(p-p)}}{2\sqrt{2}} = \frac{V_{CC}}{2\sqrt{2}} \quad I_{ce} = \frac{I_{ce(p-p)}}{2\sqrt{2}} = \frac{I_{CQ}}{2\sqrt{2}}$$



$$\eta = \frac{ac \quad output \quad power}{dc \quad input \quad power} \times 100\% = \frac{P_o(ac)}{P_i(dc)} \times 100\% \qquad \begin{array}{c} \text{This circuit is rarely used due to poor} \\ \text{efficiency.} \end{array}$$

$$\eta = \frac{V_{ce} \times I_{ce}}{V_{cc} \times I_{cQ}} \times 100\% = 25\%$$

That is 75% of the power supplied by the sources is dissipated in the transistors. This is a waste of power, and it leads to a potentially serious heating problems with the transistors.

Transformer coupled Class A amplifier

Lower dc power loss due to very small resistance of transformer primary coil

Impedance matching

The relationship between the primary and secondary values of voltage, current and impedance are summarized as:

$$\frac{N_1}{N_2} = \frac{V_1}{V_2} = \frac{I_2}{I_1}$$
$$\left(\frac{N_1}{N_2}\right)^2 = \frac{Z_1}{Z_2} = \frac{Z_1}{R_1}$$



 N_1, N_2 = the number of turns in the primary and secondary V_1, V_2 = the primary and secondary voltages I_1, I_2 = the primary and secondary currents Z_1, Z_2 = the primary and seconadary impedance ($Z_2 = R_L$)

Efficiency: Transformer coupled Class A amplifier



Principal advantage – lower distortion than Class C, B & AB.

Principal disadvantage – lower power efficiency than Class C, B & AB.

The transformer is subject to various power losses. Among these losses are couple loss and hysteresis loss. These transformer power losses are not considered in the derivation of the $\eta = 50\%$ value.

Supplement slide: How to draw AC and DC load line for Transformer coupled Class A amplifier



Class B Power amplifier

If the collector current flows during the half cycle of the signal only, the power amplifier is known as class B amplifier.

> To have this kind of operation, Q point must be located at cut-off.



Transistor cut off $(i_c = 0)$ if: $v_l + V_B < 0.7V$

A 2nd class B BJT is needed to conduct for the negative v_l cycle.

Class B Push-Pull amplifier



¹⁵

Efficiency: Push Pull Class B Power amplifier





$$P_i(dc) = V_{CC}I_{DC} = \frac{V_{CC}I_C}{\pi} \qquad P_o(ac) = V_{ce} \times I_{ce}$$

$$V_{ce} = \frac{V_{ce-p}}{\sqrt{2}} = \frac{V_{CC}}{2\sqrt{2}} \qquad I_{ce} = \frac{I_{ce-p}}{\sqrt{2}} = \frac{I_C}{\sqrt{2}}$$

$$\eta = \frac{P_o(ac)}{P_i(dc)} \times 100\% \qquad \qquad \eta = \frac{V_{ce} \times I_{ce}}{V_{cc} \times I_{cQ}} \times 100\% = 0.25\pi\% = 78.5\%$$

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http://books.google.co.in/books?id=mrs1lwRTzo0C&pg=PR12&lpg=PR12&dq=frequency+response+of+multistage+ amplifiers&source=bl&ots=7vQ4XNKqom&sig=wsE3NpE8kja8FrqpKindgL_caNY&hl=en#v=onepage&q=frequency %20response%20of%20multistage%20amplifiers&f=false

